

ABSTRACT OF THE DISCLOSURE

A display panel driving device has a simple substrate structure. Amorphous silicon material or organic semiconductor material can be used for the substrate structure. A data code wiring group which is the base of row addresses of the display panel, and an address electrode line wiring group, intersect each other. The two wiring groups sandwich a compound layer of an insulating film and a semiconductor film. By providing disconnected portions at prescribed intersecting points on the address electrode lines, MOS type transistors are formed at the disconnected portions. A logic circuit is configured for row address decoding, in which MOS type transistors are connected in series to a single address electrode line.